



JPW

In re the Application of: **TAKESHITA, Mitsunori, et al.**

Group Art Unit: 1792

Serial No.: 10/593,282

Examiner: **Nathan K. FORD**

Filed: **April 22, 2008**

P.T.O. Confirmation No.: 8499

For: **SUBSTRATE PROCESSING APPARATUS AND MANUFACTURING METHOD
FOR A SEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 4, 2009

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached. These documents were cited by an Office Action dated April 14, 2009 corresponding to Japan patent application No. 2006-528655 with English translation. A copy of the Office Action is also attached.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following the filing date or the entry of the national stage of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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PATENT & TRADEMARK OFFICE

Enclosures: PTO-1449
References (4)
Office Action dated April 14, 2009 corresponding to Japan patent application
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